

REMARKS

Claims 1-12 are pending in this application.

Rejection of Claims 6 and 11 under 35 USC § 112

Claims 6 and 11 are rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the enablement requirement.

Claims 6 and 11 have been amended in accordance with the comments of the Examiner to recite that “a predetermined number of bits is used to control said mute circuit.” In view of the amendments to claims 6 and 11, it is respectfully submitted that this rejection is satisfied and should be withdrawn.

Rejection of Claims 1-5, 7-8 and 10 under 35 USC § 102(e)

Claims 1-5, 7-8 and 10 are rejected under 35 U.S.C 102(e) as being anticipated by Renner et al. (U.S. Patent No. 6,219,107).

The present claimed invention recites a method and apparatus for providing television receiver alignment functions. The method and apparatus is designed for electronically aligning signals within the receiver by controlling support circuitry for a module comprising a tuner, IF as well as a stereo decoder. A system microprocessor is coupled through an I²C bus to a DAC that controls both video alignment and picture IF mute functions. The digital-to-analog converter separates the control signal and uses certain bits to control the mute circuit and the remaining range to control the video level circuit.

Renner discloses a device for controlling automatic gain control circuits in video decoders. However, Renner neither discloses nor suggests “a mute circuit coupled to said control signal source” as in the present claimed invention. Additionally, Renner neither discloses nor suggests “a first portion of a control signal

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from said control signal source controls said mute circuit” as in the present claimed invention. In fact, as Renner is not concerned with IF mute functions and does not include the claimed mute circuit, there is no need to separate the control signal into first and second portions with the first portion controlling the mute circuit as in the present claimed invention. Furthermore Renner neither discloses nor suggests “an IF AGC loop having a level shifting circuit and an IF AGC mute circuit” as claimed in independent claim 10 of the present invention.

In view of the above remarks it is respectfully submitted that the present invention as claimed in claims 1 and 10 is not anticipated by Renner. Additionally, as claims 2-5, 7 and 8 depend from independent claim 1, dependent claims 2-5, 7 and 8 also satisfy this rejection for the same reasons as claim 1. In view of the above remarks it is respectfully submitted this rejection of claims 1-5, 7, 8 and 10 is satisfied and should be withdrawn.

Having fully addressed the Examiner's rejections, it is believed that, in view of the preceding amendments and remarks, this application stands in condition for allowance.

If, however, the Examiner is of the opinion that such action cannot be taken, the Examiner is invited to contact the applicant's attorney at the phone number below, so that a mutually convenient date and time for a telephonic interview may be scheduled.

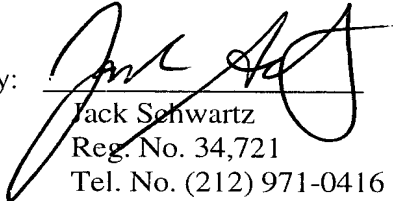
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In view of the concurrent filing of the Petition to Revive along with the requisite fees, no additional fee is believed due. However, if an additional fee is due, please charge the additional fee to Deposit Account 07-0832.

Respectfully submitted,
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